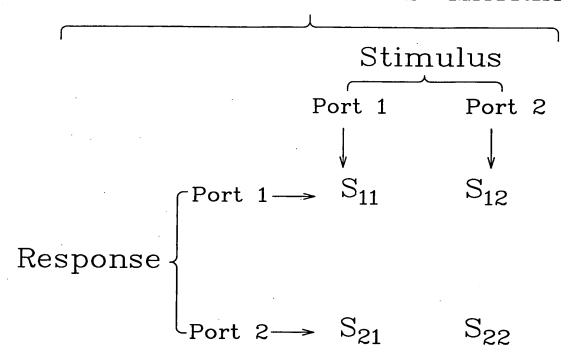
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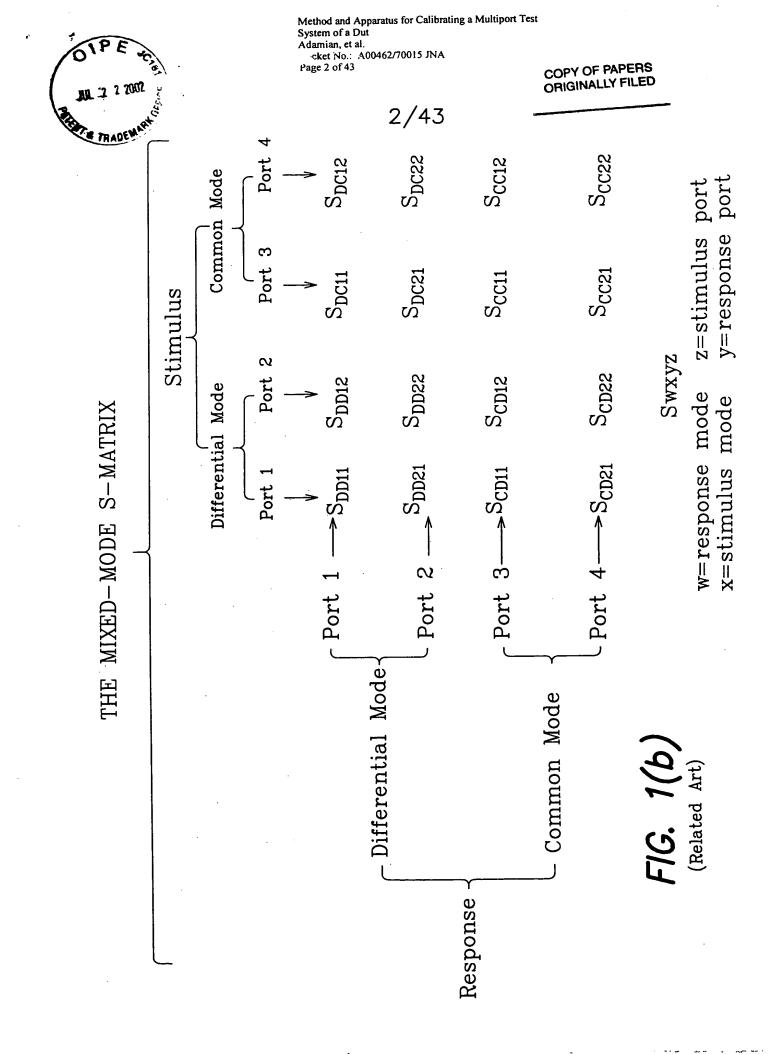
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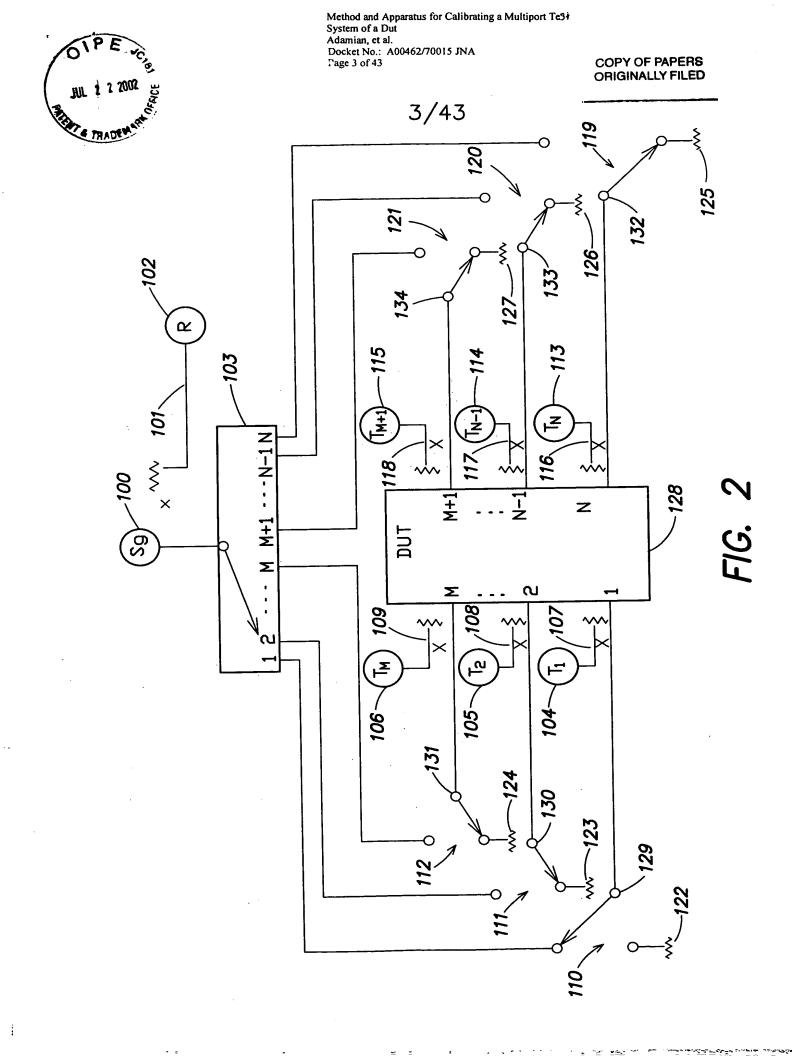


Syz z=stimulus port y=stimulus port

FIG 1(a)

(Related Art)



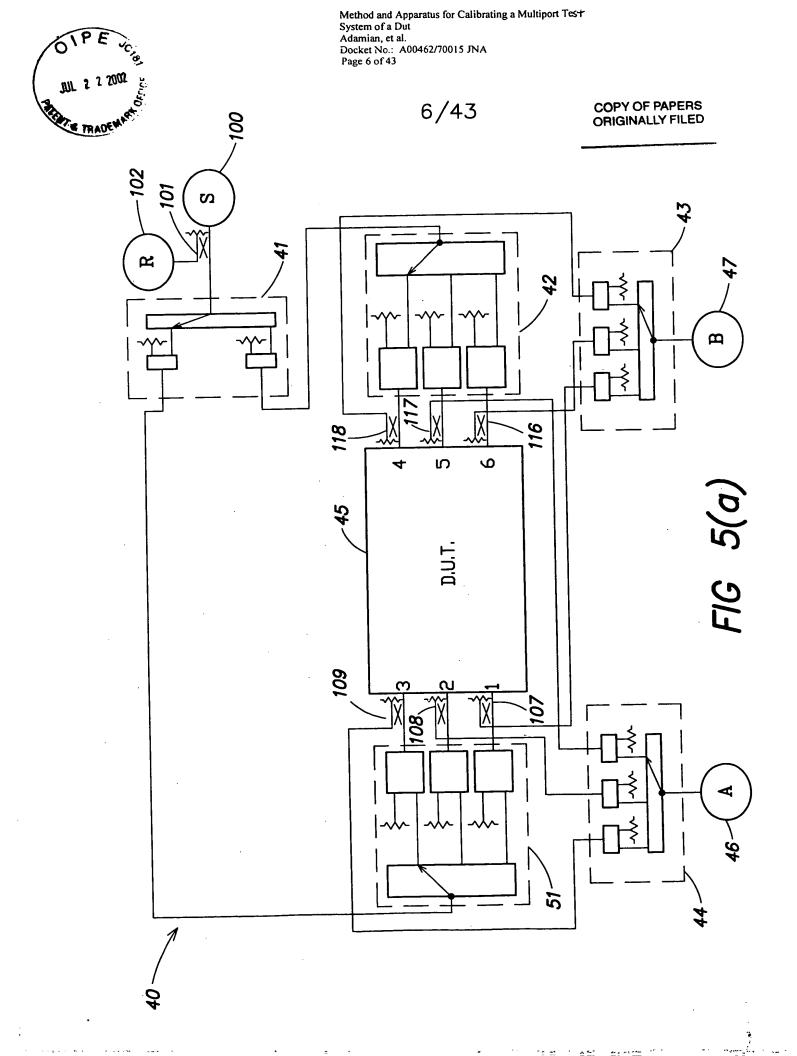


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FIG. 3

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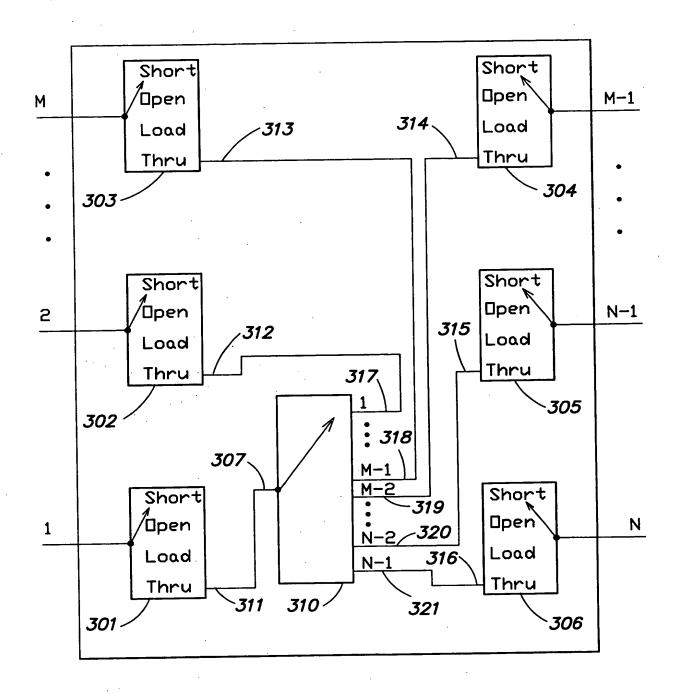
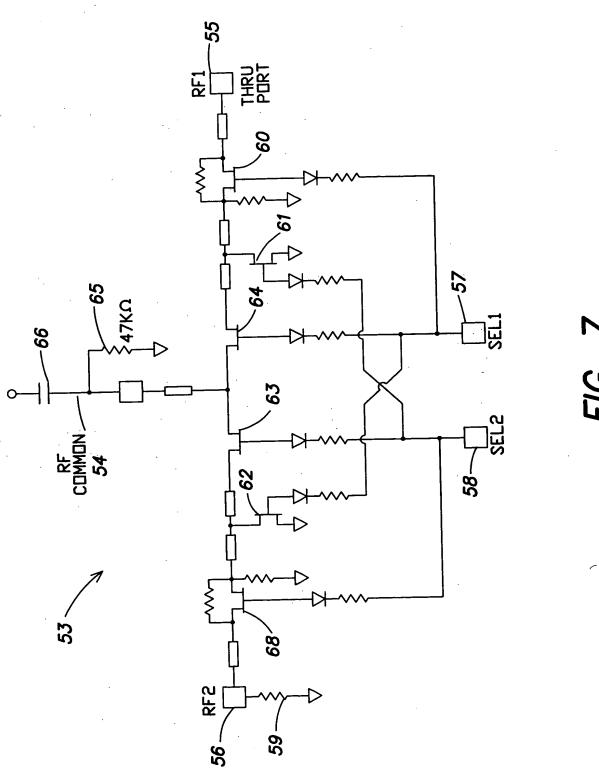


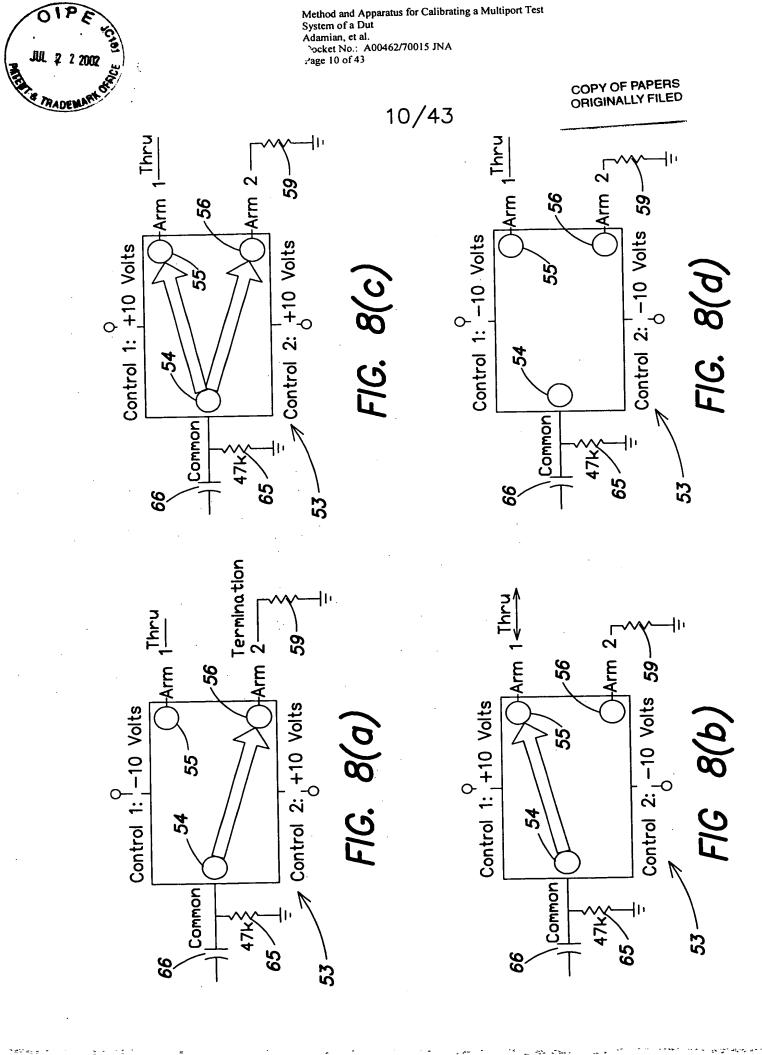
FIG 6
(RELATED ART)



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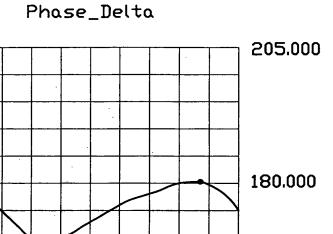
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FIG. 9

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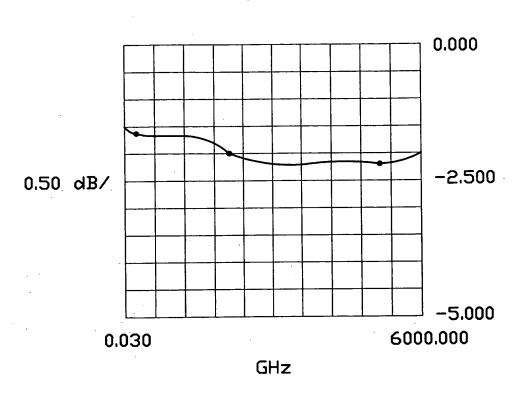


FIG. 10



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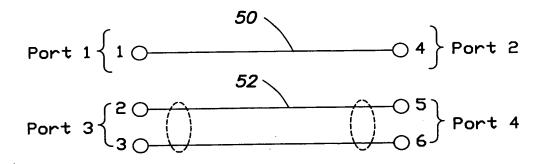


FIG. 11

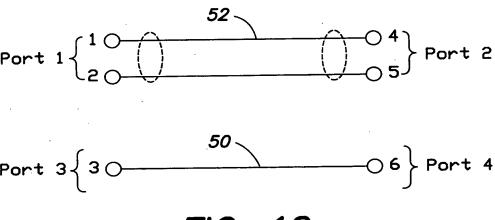


FIG. 12



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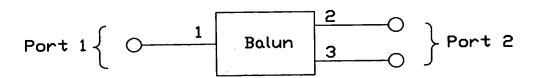


FIG. 13

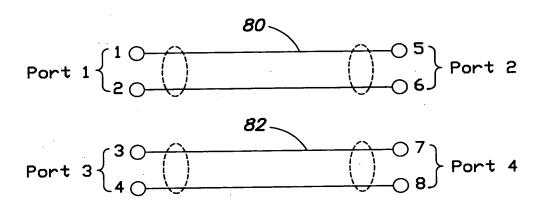


FIG. 14

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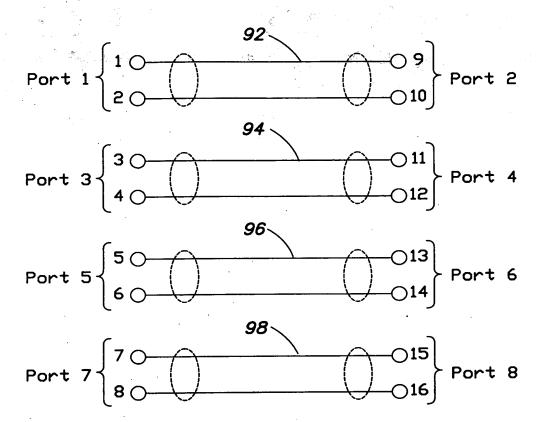


FIG. 15



Method and Apparatus for Calibrating a Multiport Test System of a Dut Adamian, et al. ~cket No.: A00462/70015 JNA

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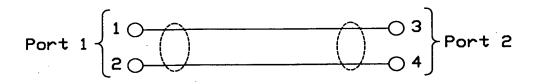


FIG. 16

Port 1
$$\begin{cases} 1 & \bigcirc & \bigcirc & 2 \\ 3 & \bigcirc & \bigcirc & 4 \end{cases}$$
 Port 2

FIG. 17



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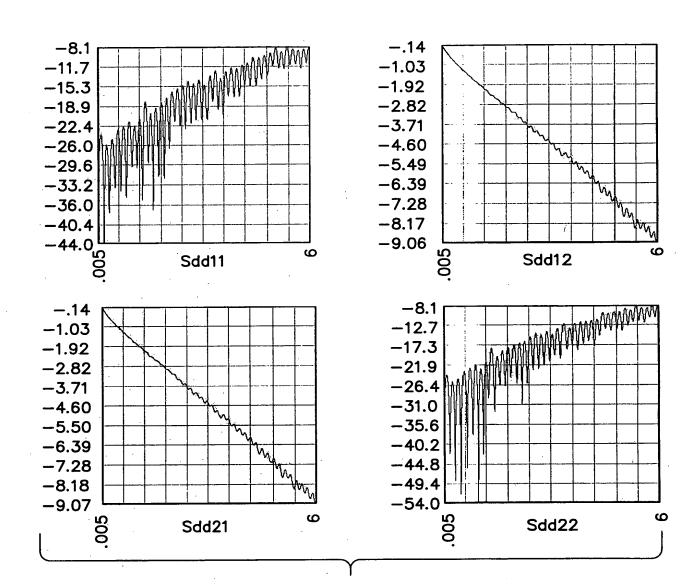


FIG. 18A



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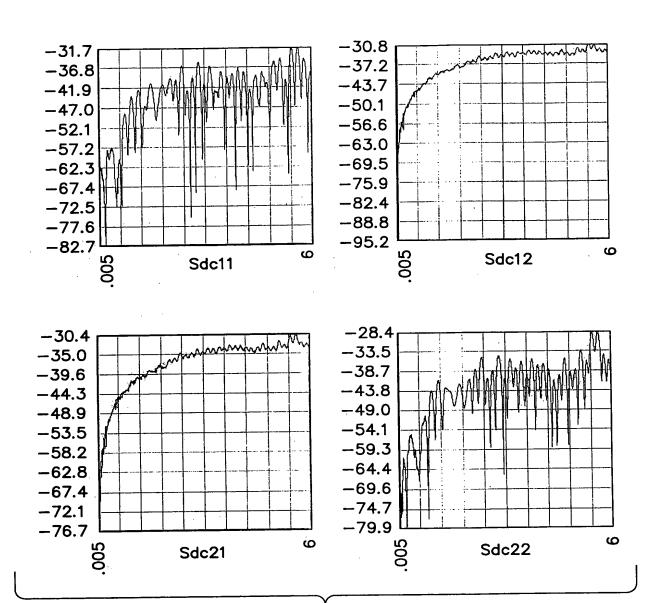


FIG. 18B



Method and Apparatus for Calibrating a Multiport Test System of a Dut Adamian, et al. Docket No.: A00462/70015 JNA Page 19 of 43

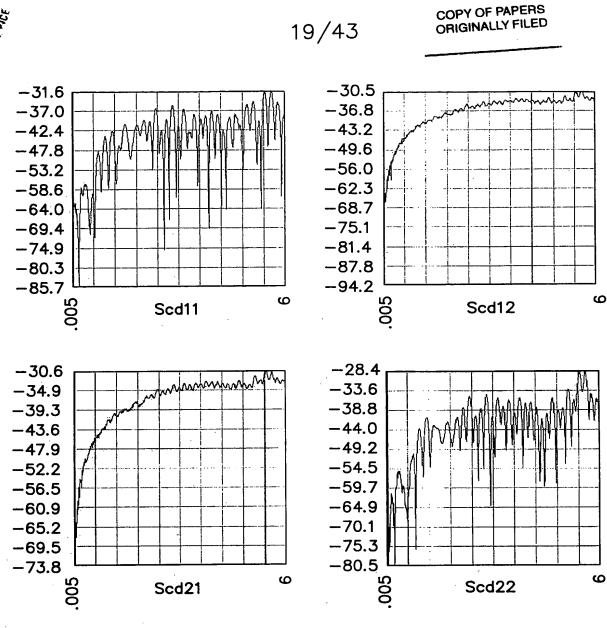


FIG. 18C

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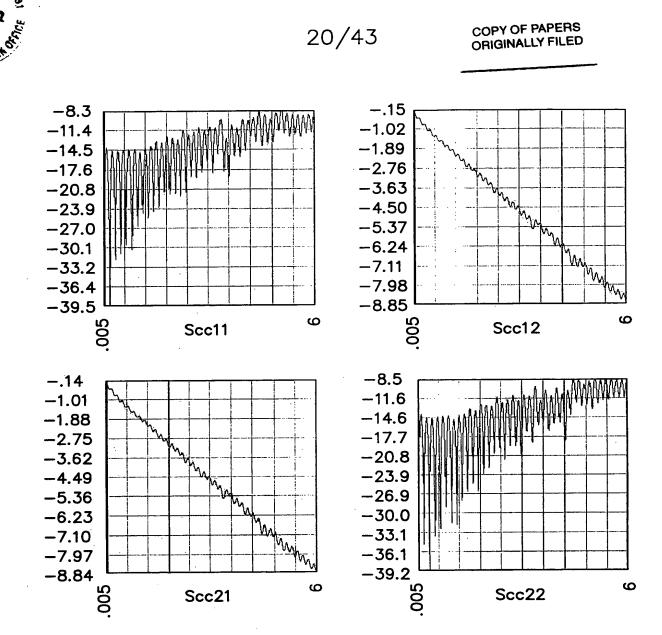


FIG. 18D



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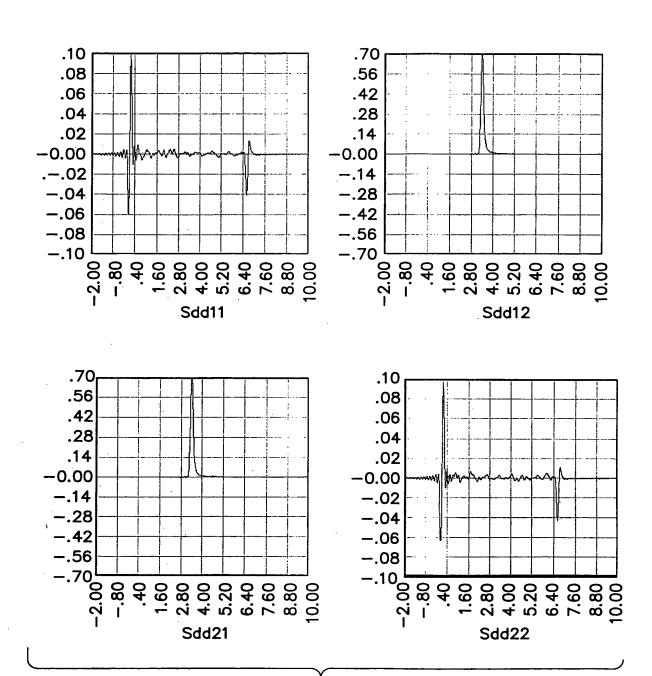


FIG. 19A

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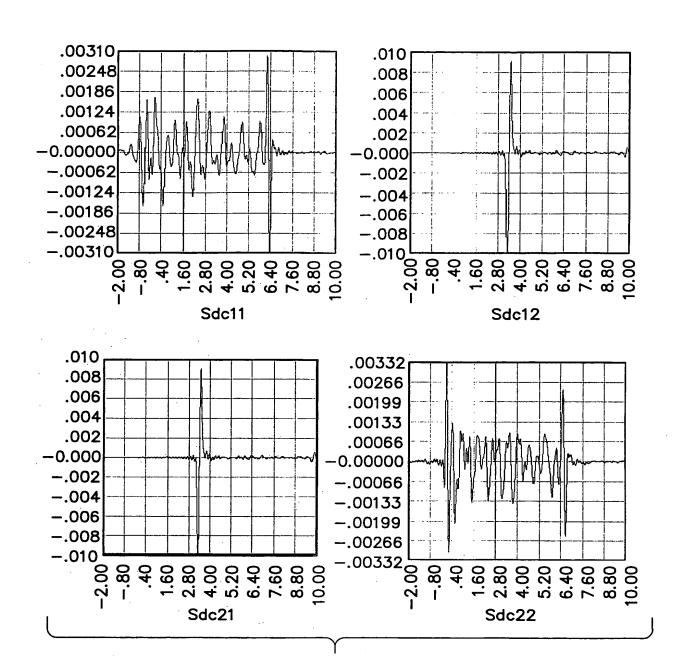


FIG. 19B



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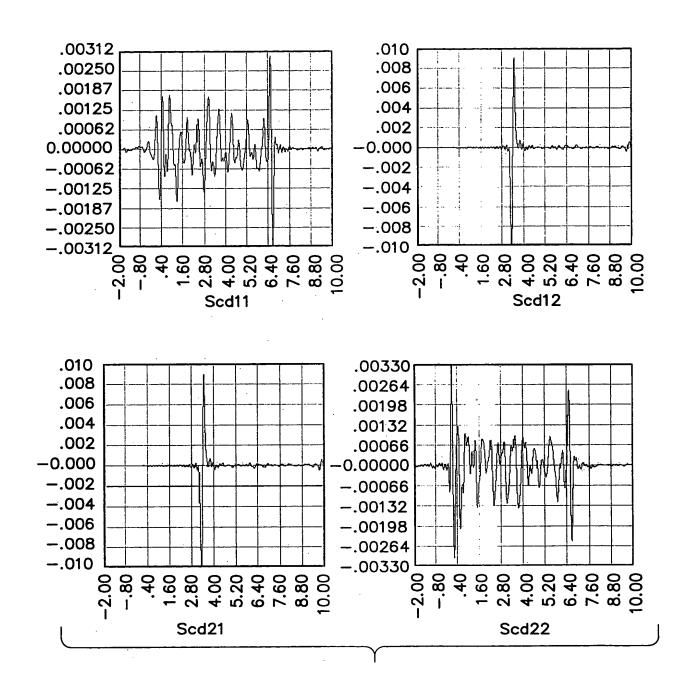


FIG. 19C



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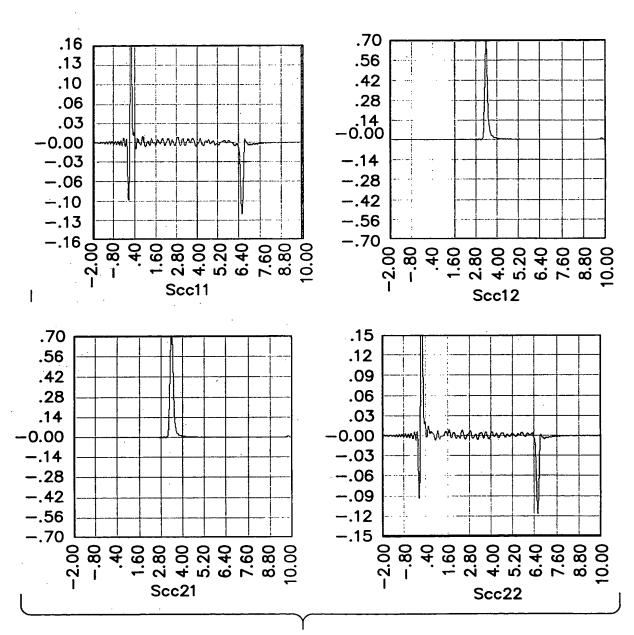
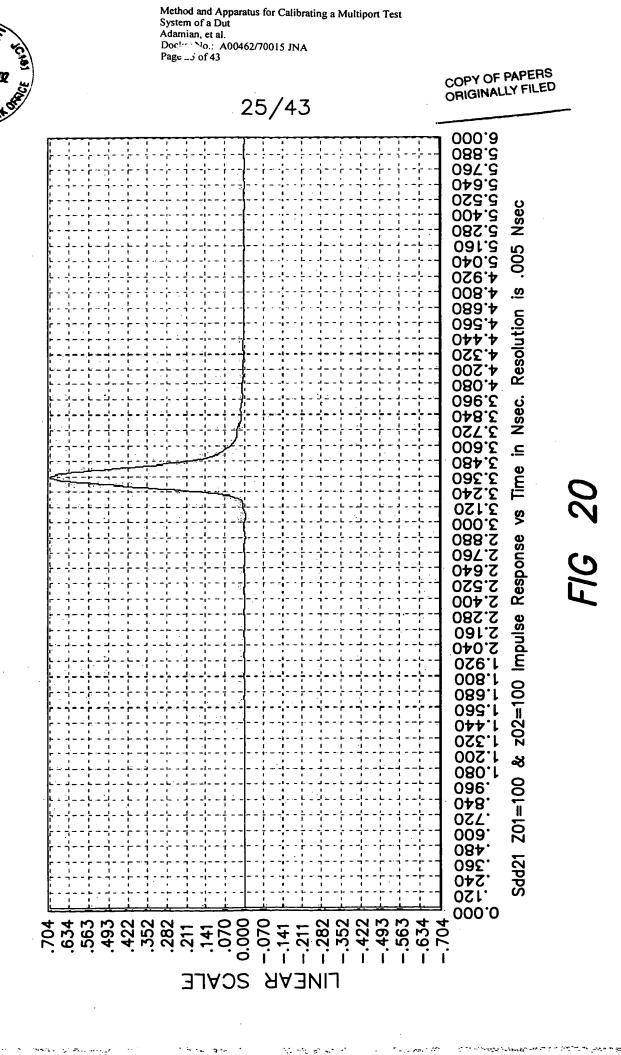


FIG. 19D



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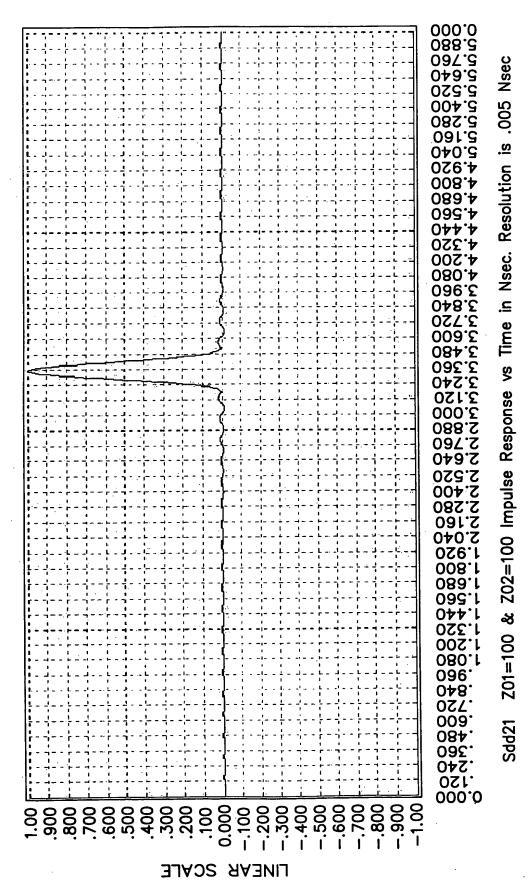


FIG 21

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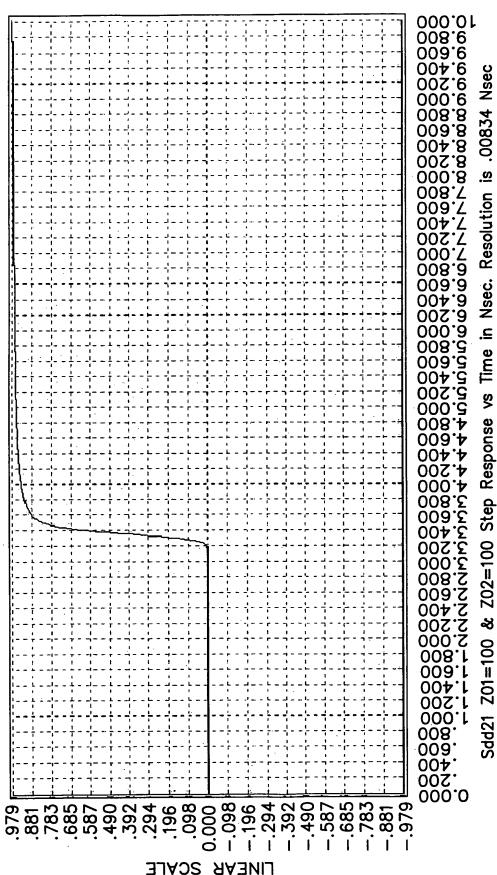


FIG 22

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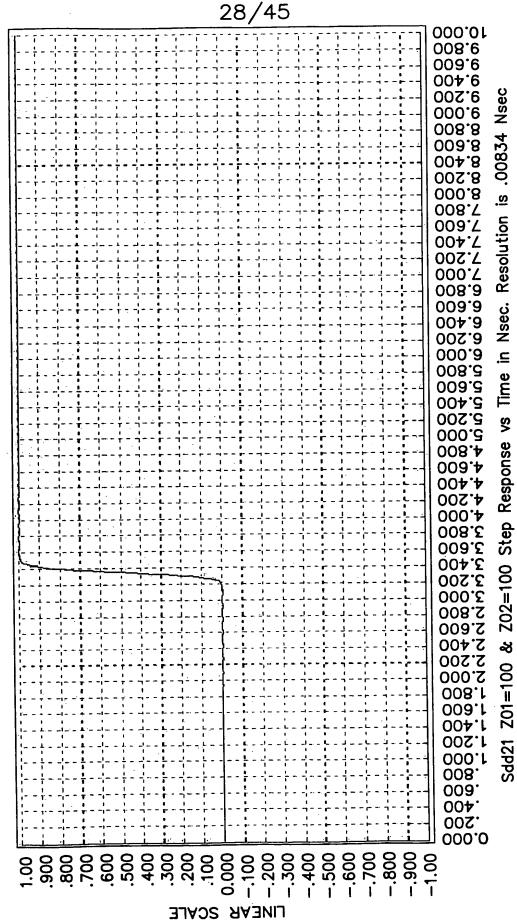
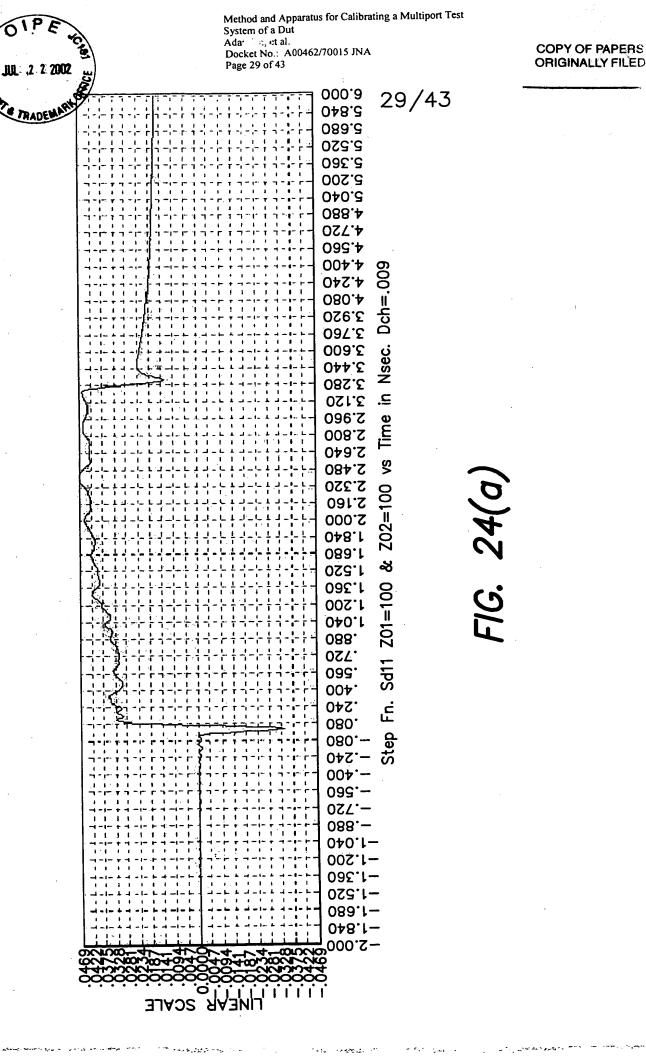
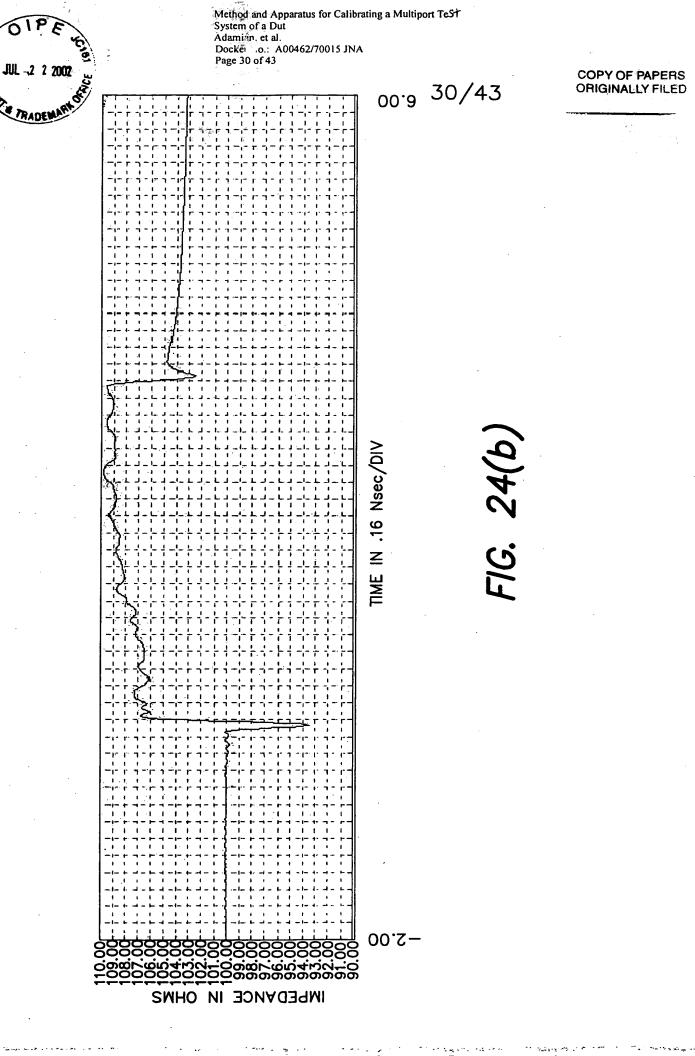


FIG 23





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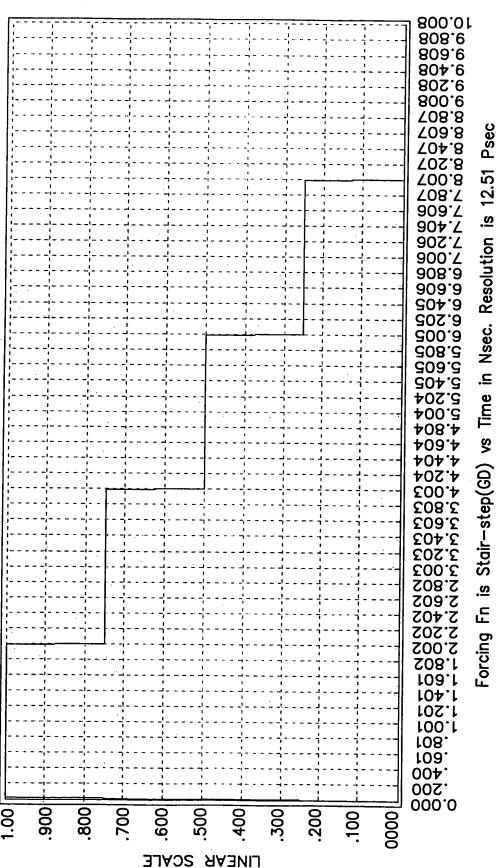
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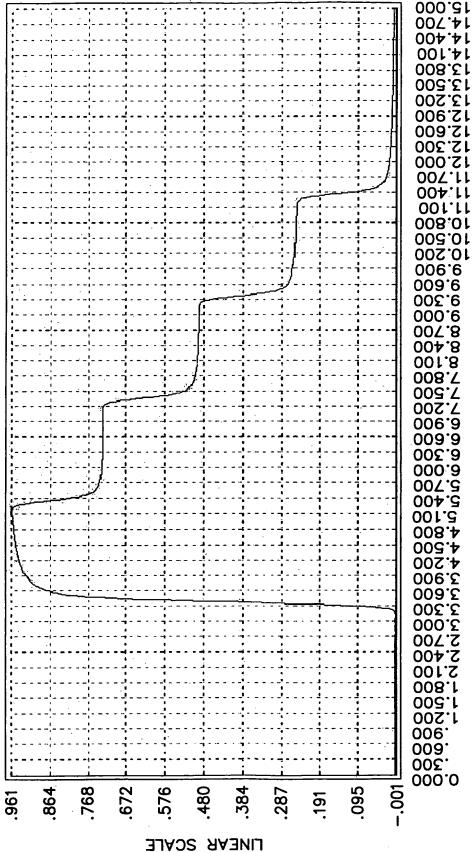


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Nsec. .⊑ Time Ş Response Function (CI) Sdd21

Resolution

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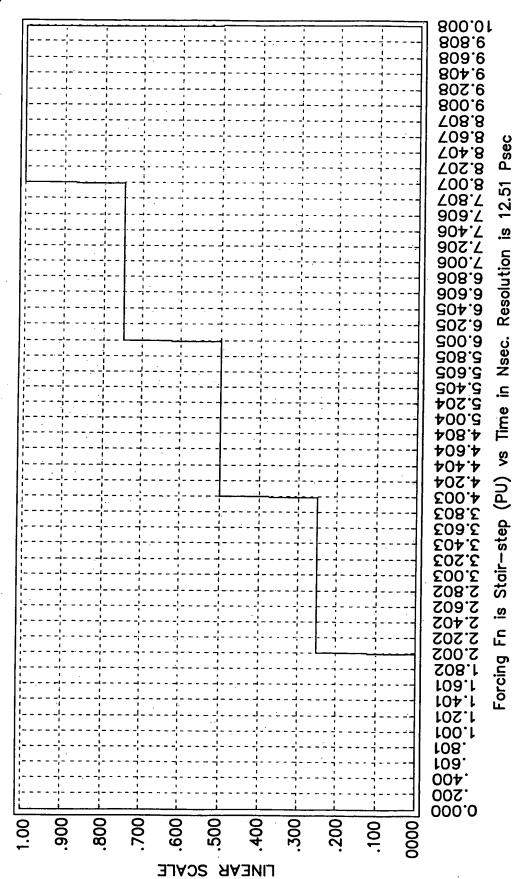
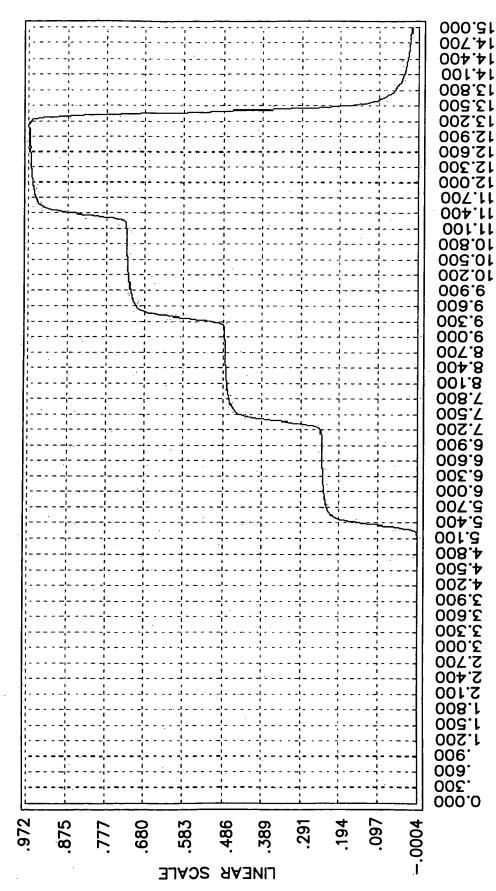


FIG 27

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Response Function (CI) vs Time in Nsec.

Sdd21

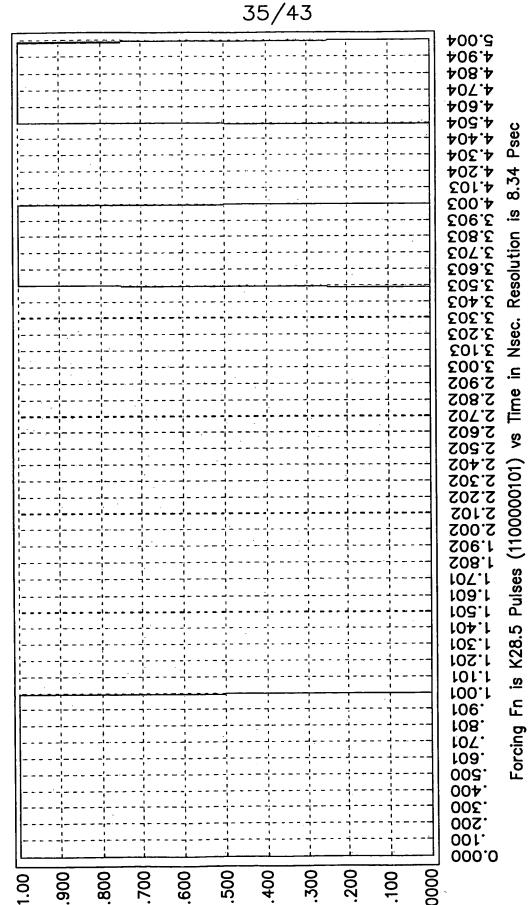
Resolution

60 513

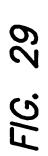
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LINEAR SCALE



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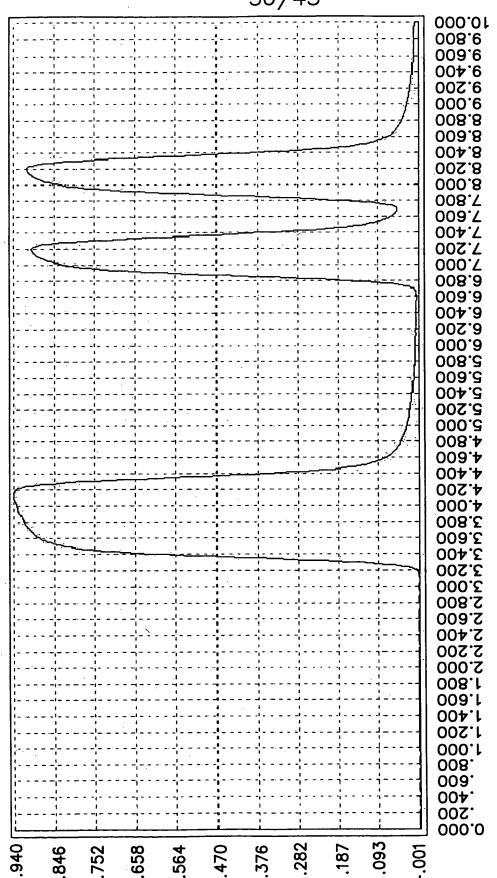


FIG 30

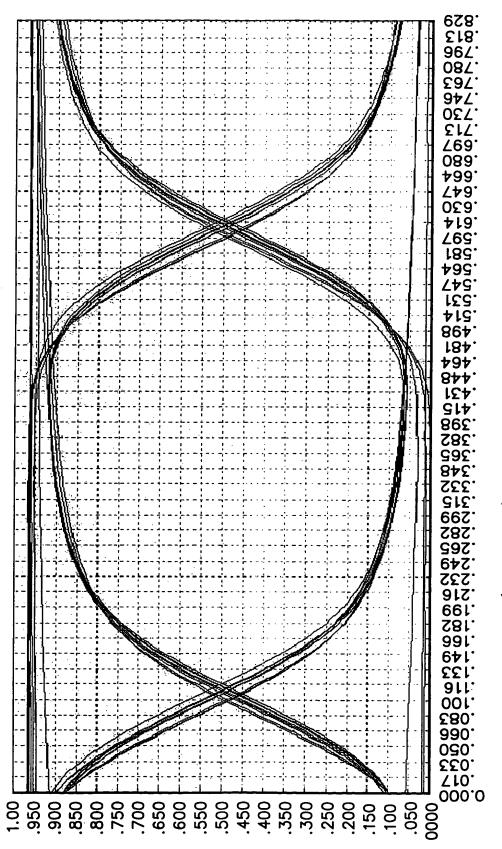
amian, et al.

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K28.5(1100000101) Digital Bit Pattern at 2Gb/s Data Rate

FIG. 31



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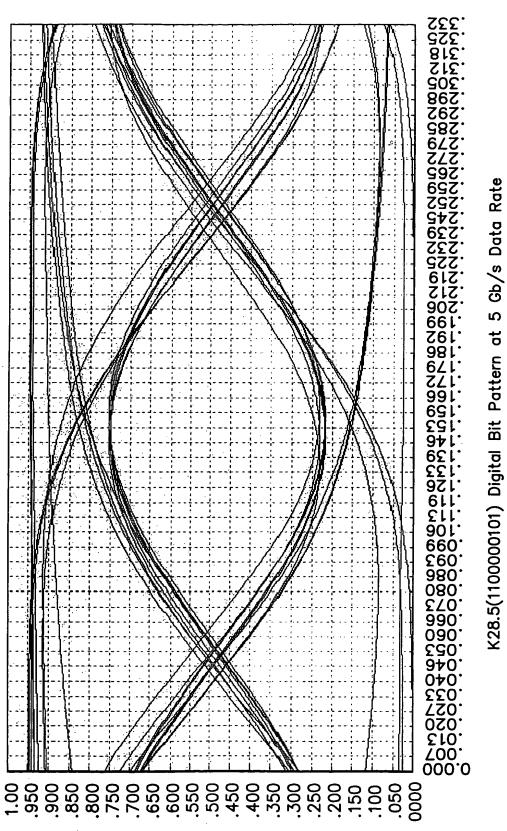


FIG 32



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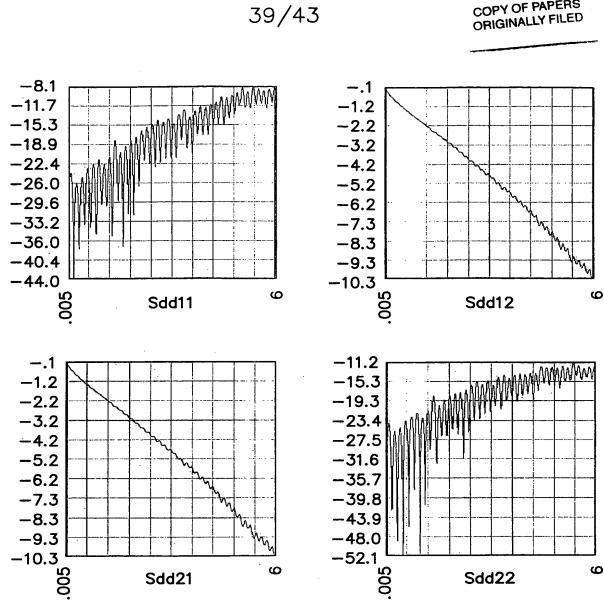


FIG. 33A



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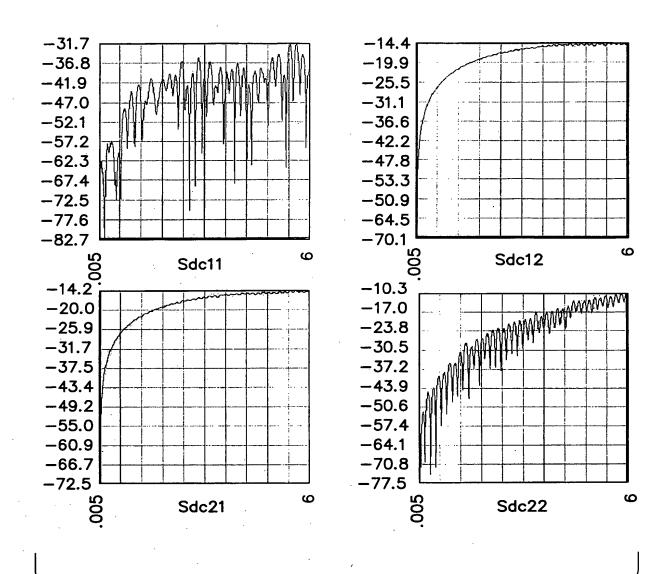


FIG. 33B



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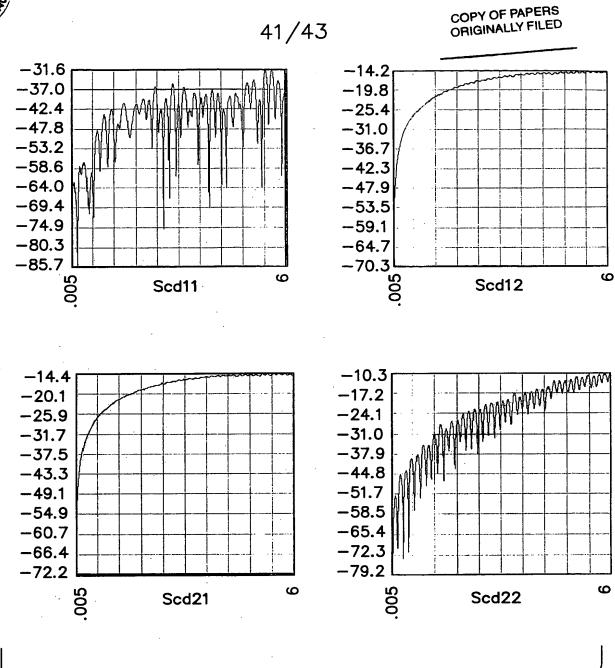


FIG. 33C

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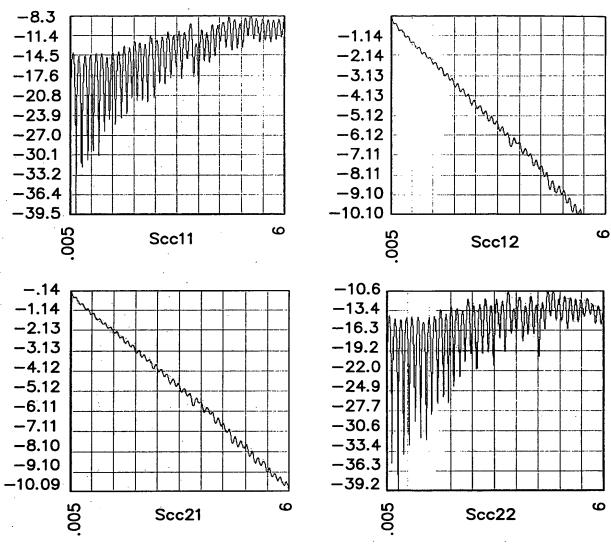


FIG. 33D

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